

Marie-Minerve Louërat received her Master degree in Electrical Engineering in 1983 and her PhD in 1986 from Université Paris Sud (Paris XI-Orsay, France). In 1986 she joined the "Centre National de la Recherche Scientifique" (CNRS), France. She started at "Fluids, Automation and Thermal Systems" laboratory (FAST) at Université Paris Sud-CNRS, while teaching Electronics. In 1992, she moved to the Computer Science Laboratory (LIP6) at University Pierre et Marie Curie (UPMC-Paris 6, France), while teaching VLSI.

Marie-Minerve Louërat's research interest is electronic design automation method and tools for analogue and mixed-signal circuit and systems. Most of her research activities have been supported by contracts, through academic and industrial cooperative projects in the framework of the FP7 and Eureka/MEDEA and Catrene projects. She published papers on static timing analysis, analogue and AMS design automation, analog-to-digital converters, AMS system modelling.

In 1998, she co-founded the Avertec Company; a spin-off from UPMC specialized in static timing analysis.

She is a member of the AMS Working Group of Accellera Systems Initiative who has standardized the AMS extension of SystemC in 2010. She has served on the Technical Program Committee of Design, Automation, and Test in Europe Conference (DATE) and several others international conferences.